Europass
Curriculum Vitae

Personal information

First name(s) / Surname(s)	**Lucan Orăşan Ioan**
Address(es)	Str. Obrejii, Nr. 45, Sat. Mărtinie, Com. Şugag, cod 517775, Jud. Alba, Romania
Telephone(s)	Mobile: 0751890027
E-mail	lucan.ionica@gmail.com
Nationality	Romanian
Date of birth	05 February 1994
Gender	Male

Personal profile

Education and training

Period	2020 – present;
Name and type of organisation providing education and training	Politehnica University Timișoara, IOSUD-UPT, Doctoral School of Engineering Studies, PhD studies in doctoral field: Electronics, Telecommunications, and Information Technologies;
Period	2018 – 2020;
Name and type of organisation providing education and training	Politehnica University Timișoara, Faculty of Electronics, Telecommunications, and Information Technologies, Master studies: Advanced Electronics in Intelligent Systems;
Period	2013 - 2017;
Name and type of organisation providing education and training	University „1 Decembrie 1918" Alba Iulia, Faculty of Informatics and Engineering, Bachelor studies, study program: Applied Electronics;
Period	2008 - 2012;
Name and type of organisation providing education and training	„Grup Școlar Industrial" Sebeș, High School;

Personal skills and competences

Mother tongue(s)	**Romanian;**

Other language(s)

Self-assessment

European level (*)

Self-assessment	Understanding		Speaking		Writing
European level (*)	Listening	Reading	Spoken interaction	Spoken production	
English	C1	C1	C1	C1	C1

Technical skills and competences	Operating PC: Windows, Microsoft Office; Informatics: ANSI C, C++ (medium-advanced level), Database management, Pascal;

Computer skills and competences	Use of software: CadSoft Eagle, MPLAB IDE, Kinetis, Simulation in Orcad Pspice & Matlab & CodeWarrior (medium level), Lazarus IDE;
Internships	Practice period 01.07.2015 – 30.07.2015 in the company SC BANCELEC SRL industrial automation domain; Summer Practice 2016 Continental Automotive Timişoara, period: 08.07.2016 – 15.09.2016;
Jobs	Period 15.08.2015 – 30.09.2015: Hardware Design assistant, SC.BANCELEC.SRL, Alba Iulia; Period 02.08.2017 – 30.09.2019: Software Developer I, SC CONTINENTAL AUTOMOTIVE ROMANIA SRL, Timișoara; Period 01.10.2019 – 30.09.2021: Software Developer II, SC VITESCO TECHNOLOGIES ENGINEERING ROMANIA SRL, Timișoara; Period 01.10.2021 – 03.03.2023: Senior Software Engineer, SC VITESCO TECHNOLOGIES ENGINEERING ROMANIA SRL, Timișoara; Period 06.03.2023 – present: Software Project Manager, SC HELLA ROMANIA SRL, Timișoara - Ghiroda;
Areas of interest	Microcontrollers, battery management systems, implementation of deep learning paradigms on embedded systems (microcontrollers), compression and optimization of deep neural networks, deep learning, image classification, and computer vision;
Extracurricular activities (Bachelor studies)	Participation to the Hardware Continental lecture, Year II of faculty, semester II - 2015; Participation to the Software Embedded Continental lecture, Year II of faculty, semester II - 2015; Participation to the Electronics Hardware in Automotive Continental lecture, Year III of faculty, semester II - 2016; Participation to the Embedded Software in Automotive Continental lecture, Year III of faculty, semester II 2016; Participation to the Continental Technical Competition 2016 Sibiu; Participation to the In-Extenso 2017: 5-6mai, "1 Decembrie 1918" University of Alba Iulia: 1ST Place, with the project: Case study for BLDC motor drive; 2ST Place, teamwork with the project: Ambient sensor for smart home servers using the One Wire communications protocol.
Projects (Bachelor studies)	Buck converters: 24VDC to 3.3V, 5V, 12V, 0V --- (+24V),0V --- (-24V) all in one printed circuit board; Development board with microcontroller dsPIC. „**Car windows central locking system"** Description: DC motor control in H-bridge with power MOSFET – IRF and PWM command with variable duty-cycle (dsPIC microcontroler).
Publications (PhD studies)	[1] Lucan Orăşan, I.; Seiculescu, C.; Căleanu, C.D. A Brief Review of Deep Neural Network Implementations for ARM Cortex-M Processor. Electronics 2022, 11, 2545. https://doi.org/10.3390/electronics11162545 [2] I. L. Orăşan, A. -I. Bublea and C. D. Căleanu, "Deep Learning-Based Eye Gaze Estimation for Automotive Applications Using Knowledge Distillation," in IEEE Access, vol. 11, pp. 120741-120753, 2023, doi: 10.1109/ACCESS.2023.3325134. [3] I. Lucan Orăşan and C. Daniel Căleanu, "ARM Embedded Low Cost Solution for Implementing Deep Learning Paradigms," 2020 International Symposium on Electronics and Telecommunications (ISETC), Timisoara, Romania, 2020, pp. 1-4, doi: 10.1109/ISETC50328.2020.9301130. [4] I. L. Orăşan, C. Seiculescu and C. D. Caleanu, "Benchmarking TensorFlow Lite Quantization Algorithms for Deep Neural Networks," 2022 IEEE 16th International Symposium on Applied Computational Intelligence and Informatics (SACI), Timisoara, Romania, 2022, pp. 000221-000226, doi: 10.1109/SACI55618.2022.9919465.

| Driver's license | For B category, Year 2013. |